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UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kalnitsky, et al.      Atty Docket: 60305-307201  
Application No: 09/882,538      Group Art Unit: 2811  
Filed: June 15, 2001      Examiner: Farahani, D.

COPY OF PAPERS  
ORIGINALLY FILED

Title: **METHOD FOR FORMING A SUPER SELF-ALIGNED HETERO  
JUNCTION BIPOLAR TRANSISTOR**

Commissioner of Patents  
Washington, D.C. 20231

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FORMAL DRAWINGS TRANSMITTAL

Enclosed herewith for filing in the above-identified application are the following:

- ☒ Letter to the Official Draftsman;
- ☒ Formal Drawings (9 Sheets); and
- ☒ Postcard for date-stamped return as confirmation of receipt of these items.

Total are no fees due at this time. However, the Commissioner is further authorized to charge any required additional fees, or credit any overpayment, to deposit account 02-3964.

Respectfully submitted,

DATE: March 7, 2002

Brian R. Coleman  
Reg No: 39,145

OPPENHEIMER WOLFF & DONNELLY LLP  
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CERTIFICATE OF MAILING (37CFR 1.8 (a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited on March 7, 2002 with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231.

Date: March 7, 2002

Shenna Hicks



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Attn: Official Draftsman  
Commissioner of Patents  
Washington, D.C. 20231


LETTER TO THE OFFICIAL DRAFTSMAN

Dear Sir:

Subject to the approval of the Examiner, please substitute the enclosed 9 sheets of formal drawings for the informal drawings originally filed with the application.

Respectfully submitted,

DATE: March 7, 2002

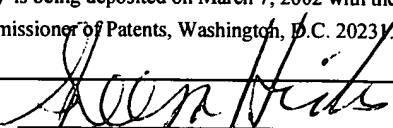
  
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Sheena Hicks